

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L8	1278	design near2 rule near2 (check\$3 or verif\$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 12:02
L9	10	((line or wire or conductor or metal or connect\$3 or interconnect\$3) with transistor with (exclud\$3)) and L8	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 12:08
L10	11	((line or wire or conductor or metal or connect\$3 or interconnect\$3) with transistor with (exclud\$3 or except\$3)) and L8	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 12:04
L11	22	((((line or wire or conductor or metal or connect\$3 or interconnect\$3) with (exclud\$3)) same transistor) and L8	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 12:18
L12	3	((((line or wire or conductor or metal or connect\$3 or interconnect\$3) with (except\$3)) same transistor) and L8	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 12:19
S1	2578	(design near2 rule) and (line near2 width)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 14:01
S2	525	((mark\$2 or sign or token or indicat\$3 or label or type or stamp) with width) and S1	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 14:02
S3	88	((mark\$2 or sign or token or indicat\$3 or label or type or stamp) with power) and S2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 14:03
S4	47	schematic and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 13:51
S5	35	(minimum near3 width) and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 14:04
S6	2607	((design near2 rule) or DRC) and (line near2 width)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 14:02

S7	536	((mark\$2 or sign or token or indicat\$3 or label or type or stamp) with width) and S6	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 14:02
S8	187	((mark\$2 or sign or token or indicat\$3 or label or type or stamp) with (power or current)) and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 14:03
S9	82	(minimum near3 width) and S8	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 14:04
S10	8116	"716"/.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 14:05
S11	21	S9 and S10	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 09:19
S12	2	("6769103").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/20 10:53
S13	1276	design near2 rule near2 (check\$3 or verif\$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 10:58
S14	9	((line or wire or conductor or metal or connect\$3 or interconnect\$3) with (mark\$3 or label\$3 or flagg\$3 or tag\$4 or index\$3) with width with (power or current)) and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 11:04
S15	15	((line or wire or conductor or metal or connect\$3 or interconnect\$3) with (mark\$3 or label\$3 or flagg\$3 or tag\$4 or index\$3) with width) and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 11:39
S16	30	((line or wire or conductor or metal or connect\$3 or interconnect\$3) with (mark\$3 or label\$3 or flagg\$3 or tag\$4 or index\$3)) same width) and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 12:01
S17	18	((("5410490") or ("5404310") or ("5349542") or ("5202889") or ("5202841") or ("5150308") or ("5062054") or ("5050091") or ("4827428")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/20 15:09

S18	20	"5581475"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 15:09
S19	38	S17 or S18	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 15:09
S20	0	((adjacent or above or neighbour\$3 or over) near4 transistor with exclud\$3) and S19	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 15:50
S21	4	((adjacent or above or neighbour\$3 or over) near4 transistor) and S19	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 15:50
S22	22	("5706295") or ("5963729") or ("6038020") or ("6038383") or ("6078737") or ("6115546") or ("6295627") or ("6425113") or ("6470477") or ("6516451") or ("6546540").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/20 15:49
S23	0	((adjacent or above or neighbour\$3 or over) near4 transistor with exclud\$3) and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 15:50
S24	0	((adjacent or above or neighbour\$3 or over) near4 transistor) and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 15:50
S25	1	((adjacent or above or neighbour\$3 or over) with transistor) and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 15:50